

ECE/CS 757 Project Presentation Signup Sheet

Monday May 4 & Wednesday May 6th 11:00am-12:15pm

Attendance is required at all presentations

Date/Time	Project Title	Team Members
5/4 11:00	Interconnect Evaluation of Multicore Designs Executing Vector Applications	Sean Franey, Hsiang-kuo Tang, Paula Aguilera
5/4 11:15	Speculating Past Barriers with Transactions	Polina, Venkat, Ramya
5/4 11:30	Implementation of Cache Coherence Protocols	Tara Mohan, Jeremy Tanumihardjo, Wee Chin Tan
5/4 11:45	Exposing Parallelism in Multiprocessor Deterministic Replay	Arkaprava Basu, Rathijit Sen
5/6 11:00	Methods for Deterministic Replay	Mike Smith
5/6 11:15	Implementing Power6 Coherence Protocol	Somayeh Sardashti, Hamid Reza Ghasemi
5/6 11:30	Power Characterization of NoC Router Microarchitectures	Amin, Shreesha, Ben, Vaishali
5/6 11:45	Design Space Exploration of NoC Topologies	Amit, Shoaib, Janaki, Kanchan