ECE 757 Advanced Computer Architecture II – Course Reader
Spring 2013, Professor Mikko H. Lipasti

Introduction
01. H. Sutter and J. Larus, Software and the Concurrency Revolution, ACM Queue, September 2005

Cores, Multicores, and Multithreading

Multiprocessor Software and Instruction Set Architecture

Memory Systems and Cache Coherence

Memory Consistency

Transactional Memory

Interconnects
SIMD and MPP
17. The Cray-1 Computer System. Cray Research Inc.

Clusters and GPGPUs